



Subclass	454
Class	35

ISSUE CLASSIFICATION

**PATENT NUMBER**

## **U.S. UTILITY Patent Application**

(2)	O.I.P.E. 47	PATENT DATA
SCANNED <u>SBS</u>	Q.A. <u>DW</u>	

APPLICATION NO.	CONT/PRIOR:	CLASS	SUBCLASS	ART UNIT	EXAMINER/ER
09/523990		235	454	2876	WALSH, J.D.

APPLICANTS  
Mou-Fishung Lin  
Chien Lee

**Method of manufacture and identification of semiconductor chip marked for identification with internal marking indicia and protection thereof by non-black layer and device produced thereby**

PTO-2040  
12/89

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print.Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	15	38	1	23	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	<i>D. W. [Signature]</i>			NOTICE OF ALLOWANCE MAILED <i>3-303</i>	
				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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